

ISSUE CLASSIFICATION	
Class	Subclass

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

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Q.A. 10

5C906

APPLICATION NO. 09/780207	CONT/PRIOR D	CLASS 438	SUBCLASS 678	ART UNIT 2814 2827	EXAMINER EXAMINER D. GRAYBILL
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APPLICANTS

Mirṁajid Seyyed

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Flip chip technique for chip assembly

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ _____			
ISSUE FEE				
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
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